CALL FOR PAPERS
14th International Workshop on
Microprocessor Test and Verification
(MTV 2013)

Common Challenges and Solutions
December 11-13, 2013, Hyatt Regency On Town Lake,
Austin, Texas, USA

http://mtvcon.org

General Chair: Magdy S. Abadir (Freescale, USA) – m.abadir@freescale.com
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THE PURPOSE of this workshop is to bring researchers and practitioners from the fields of verification and test together to exchange innovative ideas and to develop new methodologies to solve the difficult challenges facing us today in various processor and SOC design environments. In the past few years, some work has been done on exploiting techniques from test to solve problems in verification and vice versa. This is the 14th edition of the MTV Workshop, a testament to its success in providing an ideal environment for cross-examination of test and verification experiences and innovative solutions.

AREAS OF INTEREST include, but not limited to:

- Validation of microprocessors and SOCs
- Test/Verification of multimedia processors
- Performance testing
- High-level test generation for functional verification
- Emulation techniques
- Silicon debugging
- Low Power verification
- Formal techniques and their applications
- Verification coverage
- Equivalence checking of custom circuits
- ESL Design and Verification Methodologies
- Virtual Platforms
- Software verification
- Circuit level verification
- Switch-level circuit modeling, test and verification
- Timing validation techniques
- Path analysis for verification or test
- Design error models
- Design error diagnosis
- Design for Testability or Verifiability
- Optimizing SAT procedures with applications to testing and formal verification

AUTHOR INFORMATION: By the paper deadline authors are invited to submit PDF versions of their extended abstracts (max 2 pages) or papers (max 6 pages), with author names, affiliation, addresses, telephone and fax numbers, e-mail address, and the name of the person who will present the work if accepted. Authors of accepted papers can choose to submit a full paper (max 6 pages) that will be submitted to IEEE Xplore. The final camera ready version will be due before the workshop. Detailed instructions about the process will be provided at the time of acceptance.

Special Sessions, Panels and Tutorials: Proposals for special sessions, panels and tutorials are also invited. Please email the abstract of your proposals directly to the program chair as early as possible before paper submission deadline.

AUTHOR SCHEDULE
Paper Submission deadline: Sept 15, 2013 (FINAL)
Author Notification: Oct 15, 2013
Proposal for special sessions, panels and tutorials deadline: Nov 1, 2013
Final version due: Nov 10, 2013